

1 Inventor: **Kevin J. Torek et al.**  
2 Title: **Methods of Removing at Least Some of a Material from a**  
3 **Semiconductor Substrate**  
4  
5 Assignee: **Micron Technology, Inc.**  
6 Serial No.: **09/653,157**  
7 Filed: **August 31, 2000**



8 **INFORMATION DISCLOSURE STATEMENT**

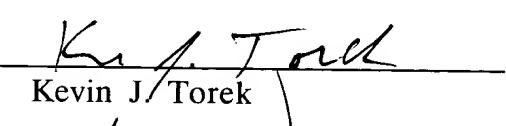
9 **PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98**

10 In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, the Examiner's  
11 attention is directed to the references listed on the attached Form PTO-1449  
12 and copies of which are attached. No admission is made regarding whether  
13 all the submitted references are prior art.

14 Citation of these references is respectfully requested.

15 Respectfully submitted,

16  
17 Date: 11-20-00 Attorney:   
18 David G. Latwesen, Ph.D.  
19 Reg. No. 38,533  
Wells, St. John

20 Date: 11-20-00 Inventor:   
21 Kevin J. Torek

22 Date: 11-28-00 Inventor:   
23 Garo J. Derderian